

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Present Application:**

Applicants : Michael R. Buckmaster et al.  
Title : SYSTEM AND METHOD FOR TESTING AN EMBEDDED  
MICROPROCESSOR SYSTEM CONTAINING PHYSICAL  
AND/OR SIMULATED HARDWARE  
Docket No. : 120073.415D1  
Date : July 25, 2001

**Prior Application:**

Examiner : Samuel Broda  
Art Unit : 2123  
Application No. : 09/024,324

Box Patent Application  
Commissioner for Patents  
Washington, DC 20231

**PRELIMINARY AMENDMENT**

Commissioner for Patents:

Please amend the above-identified application as follows:

**In the Specification:**

Amend the specification by inserting a new section before the "Technical Field"  
as follows:

-- CROSS-REFERENCE TO RELATED APPLICATION

This application is a divisional of pending United States Patent Application No.  
09/024,324, filed February 17, 1998. --

In the Claims:

Please cancel claims 1-21.

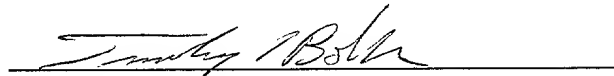
REMARKS

Claims 1-21 have been cancelled. Claims 22-28 are currently pending. Favorable consideration and a Notice of Allowance are earnestly solicited.

Respectfully submitted,

Michael R. Buckmaster et al.

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